

SECOND ANNOUNCEMENT AND CALL FOR PAPERS

Sponsored by the IEEE Electron Devices Society
and the Japan Society of Applied Physics
In Cooperation with the IEEE Solid State Circuits Society

2004 SYMPOSIUM ON VLSI TECHNOLOGY

Hilton Hawaiian Village, Honolulu, HI
June 15 - 17, 2004

The 2004 Symposium on VLSI Technology welcomes submission of papers on all aspects of VLSI technology. The scope of the symposium is:

- new concepts and breakthroughs in VLSI devices and processes
- new functional devices including quantum effect devices with possible VLSI implementation
- materials innovation for MOSFET and interconnect in VLSI
- advanced lithography and fine patterning technologies for high density VLSI
- process/device modeling of VLSI devices
- packaging and reliability of VLSI devices
- theories and fundamentals related to the above devices
- new concepts and technologies for VLSI manufacturing

SUBMISSION OF PAPERS

This year the symposia will offer online submission. Instructions for submitting online are available on the website (see web address below). Prospective authors must submit papers in the format of two pages. Those interested in submitting hard copy should contact the secretariat, as early as possible, to receive an author's kit. ***The papers are to be submitted in final form and, if accepted, will be published as submitted.*** For hard copy submission, the two-page paper and 52 DOUBLE-SIDED copies are to be sent to Widerkehr and Associates (North America and Europe) or to the Business Center for Academic Societies Japan (all countries except North America and Europe).

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To improve the probability for acceptance of a submitted paper, the paper should describe original work with specific results from experiments or simulations. **The content of the paper must not be announced or published prior to the Symposium.**

Deadline for Receipt of Papers is January 7, 2004

INFORMATION AND REGISTRATION

Prospective attendees can obtain further information and forms for Symposium registration and hotel reservations by contacting their respective secretariats. Submissions from universities are encouraged. Partial travel expense support for students who are presenting papers is available upon request.

WORLD WIDE WEB

The VLSI Symposia have a web page. It contains general information on the Symposia. The address is:

<http://www.vlsisymposium.org>

VLSI TECHNOLOGY SHORT COURSE

A one-day short course will be held on June 14, 2004. Details will be given in the VLSI Technology Symposium Advance Program, which will be issued toward the end of March, 2004.

SATELLITE WORKSHOP

A satellite workshop will be held at the same location.

June 13-14 2004 IEEE Silicon Nanoelectronics Workshop